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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants:

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and HONG, Sung-Jin

Assignee:

Samsung Electronics Co., Ltd.

Title:

THIN FILM DIODE PANEL AND MANUFACTURING METHOD

OF THE SAME

Int'l. Appl. No.:

PCT/KR2004/002742

Int'l. Filing Date:

10/28/2004

Examiner:

Unknown

Group Art Unit:

Unknown

Docket No.:

AB-1883 US

Confirmation No.:

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INFORMATION DISCLOSURE STATEMENT UNDER 37 C.F.R. § 1.97(b)

Dear Sir:

Pursuant to 37 C.F.R. § § 1.56, 1.97 and 1.98, the documents listed on the accompanying Form PTO-1449 are called to the attention of the Examiner for the above-identified application. Copies of the documents are enclosed with the exception of any U.S. patents. The documents were cited by the Austrian Patent Office in the International Search Report for International Application No. PCT/KR2004/002742, which is the international stage of the present application.

Citation of the above documents shall not be construed as:

- 1. an admission that the documents are necessarily prior art with respect to the instant invention;
 - 2. a representation that a search has been made, other than as described above; or
- 3. an admission that the information cited herein is, or is considered to be, material to patentability as defined in § 1.56(b).

No fee is believed to be required. If a fee is required for this Information Disclosure Statement, please charge Deposit Account No. 50-2257. This paper is being submitted in duplicate.

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Respectfully submitted,

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT		Applicant(s)			
(Use several sheets if necessary)		SHIN, Kyoung-Ju et al.			
		Filing Date		Group	
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U.S.	Patent Documents				
*Examiner Document Initial Number Date	Name	Class	Subclass	Filing I	
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